

FIG.2

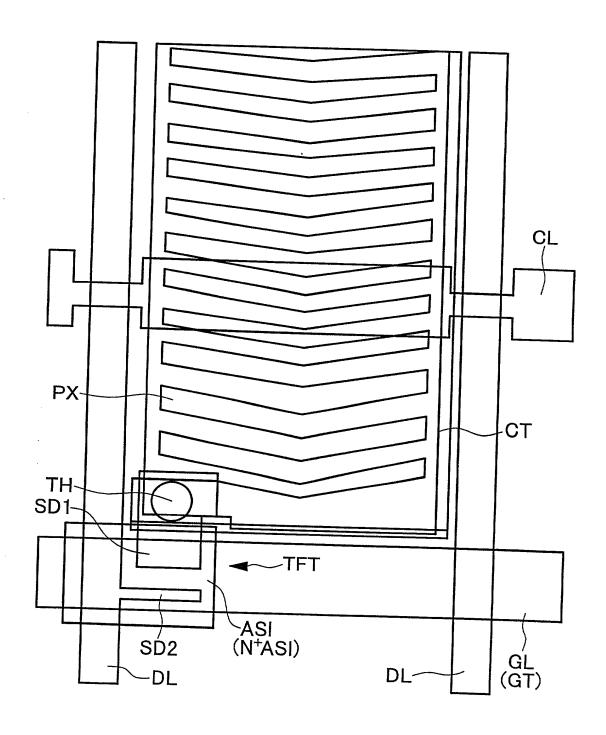


FIG.3

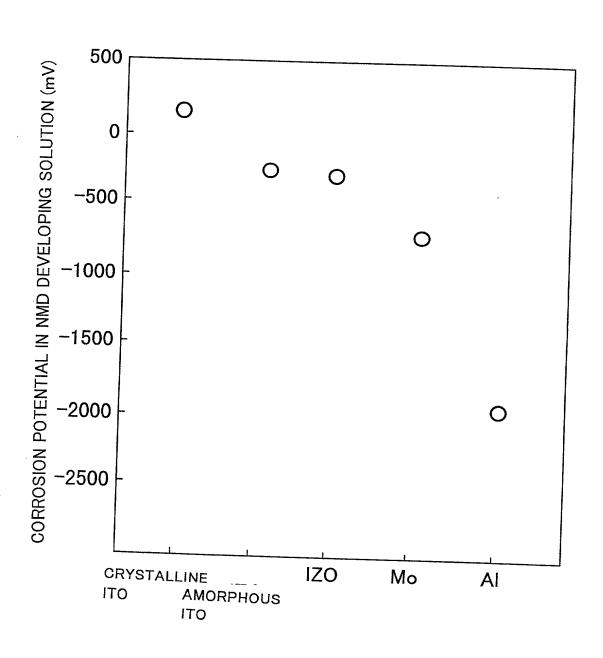


FIG.4

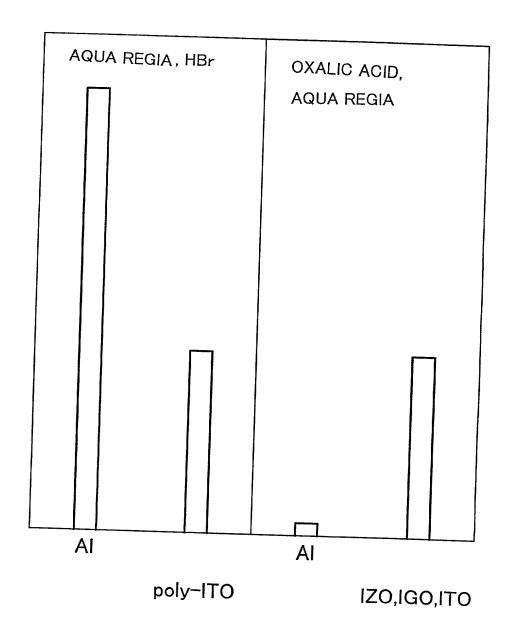
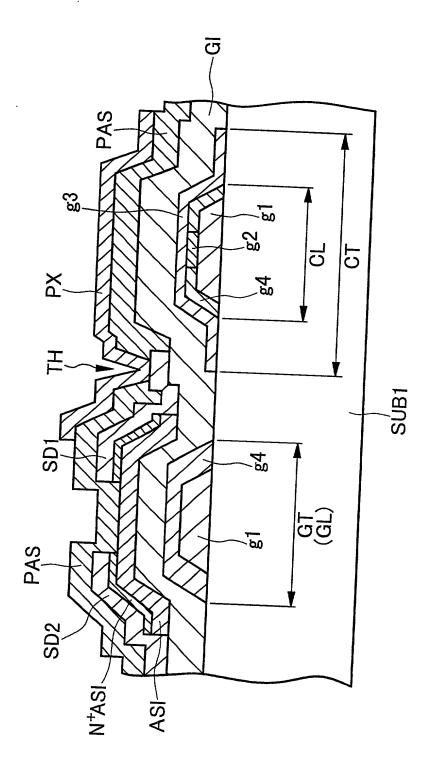
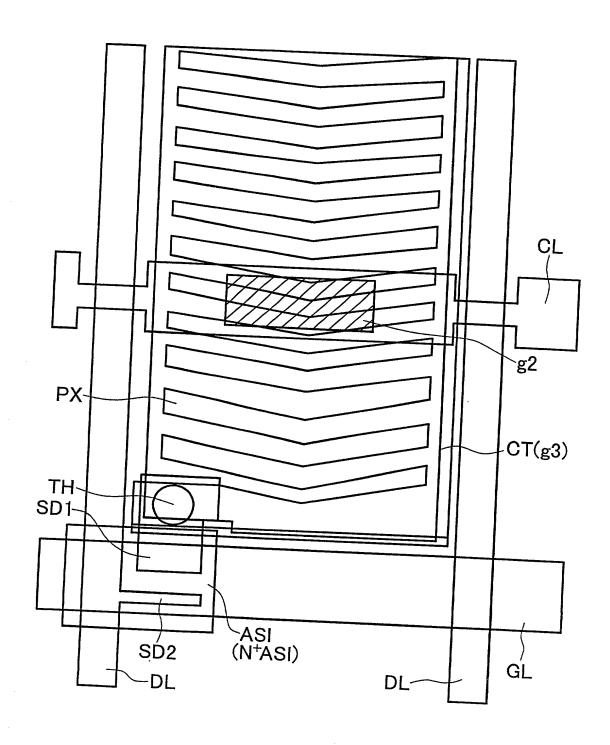


FIG.5



4

FIG.6



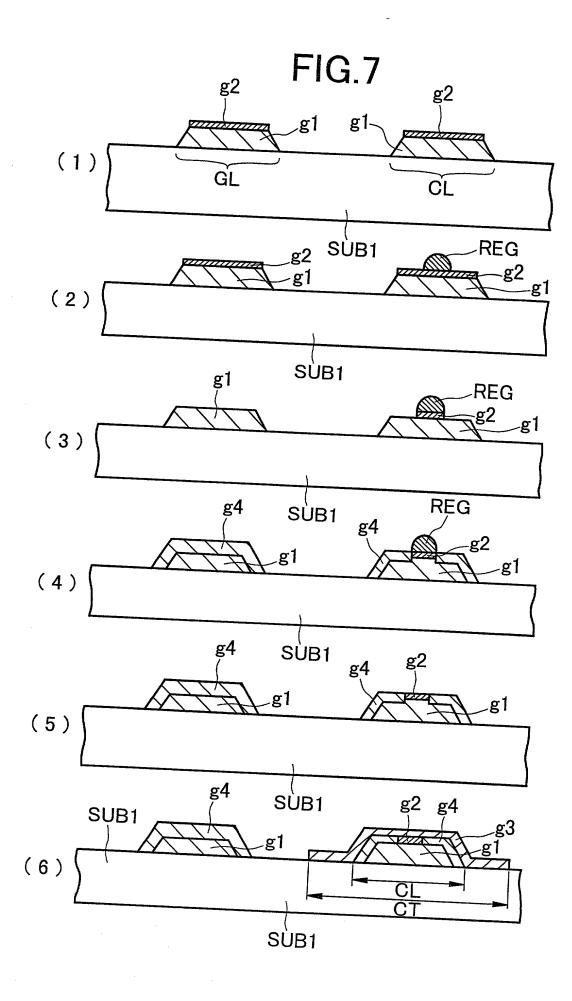


FIG.8

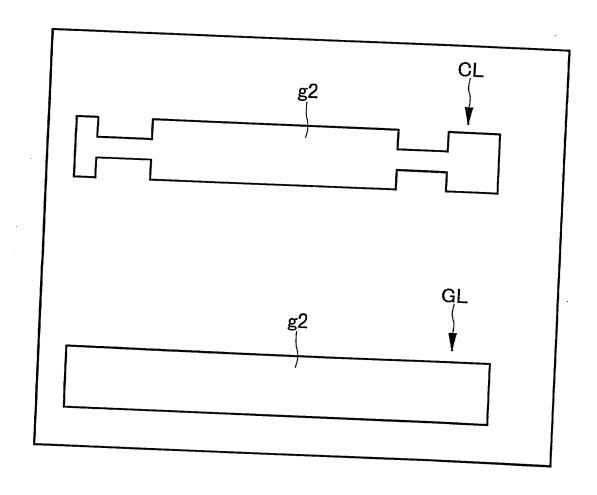


FIG.9

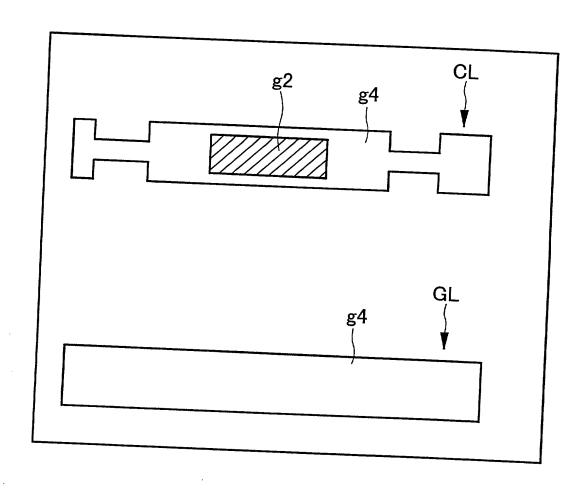


FIG.10

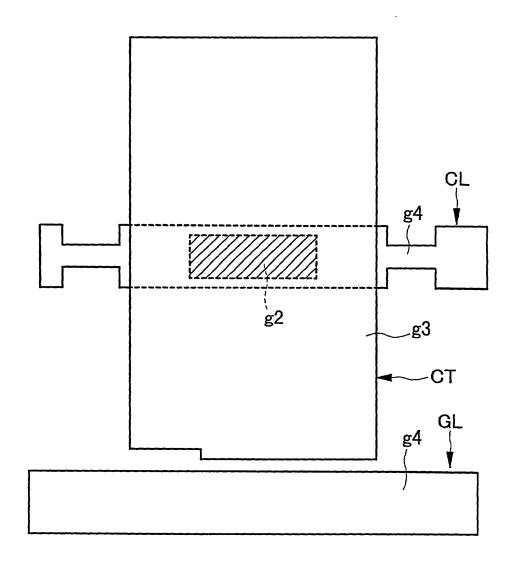
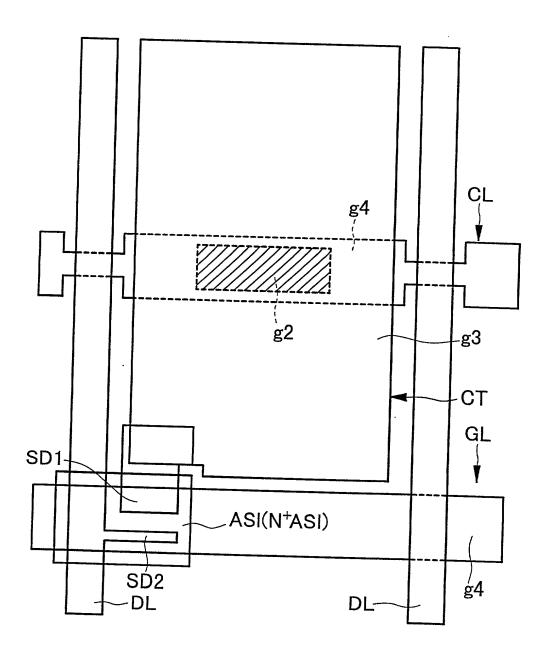


FIG.11



**FIG.12** 

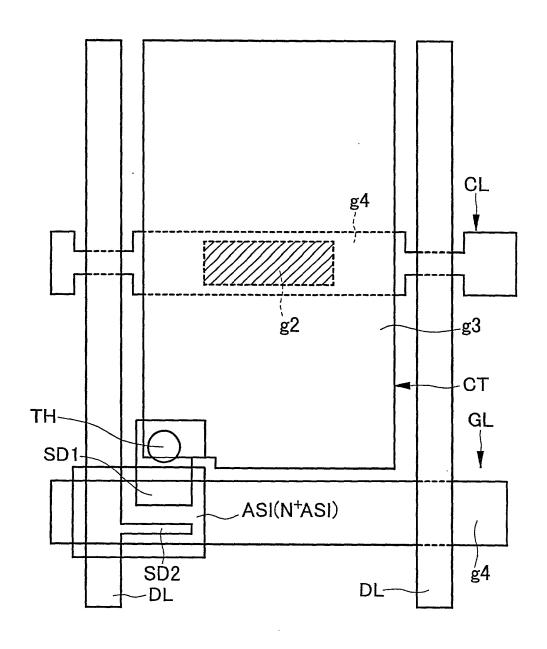


FIG.13

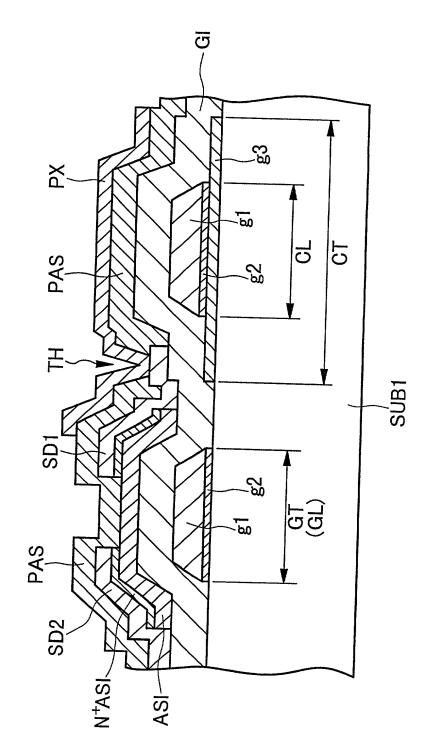
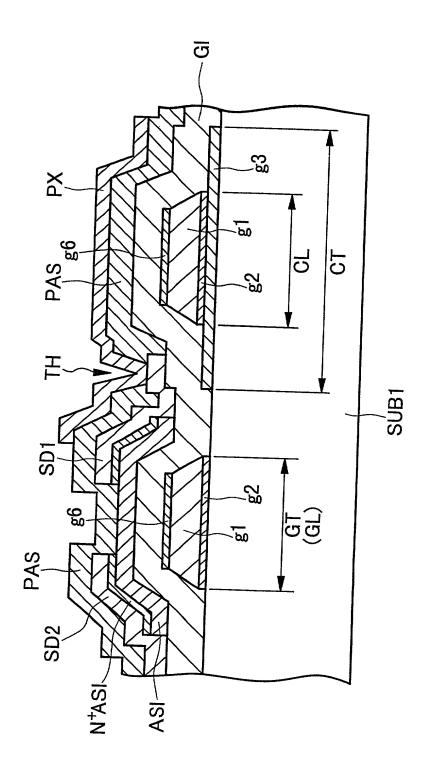
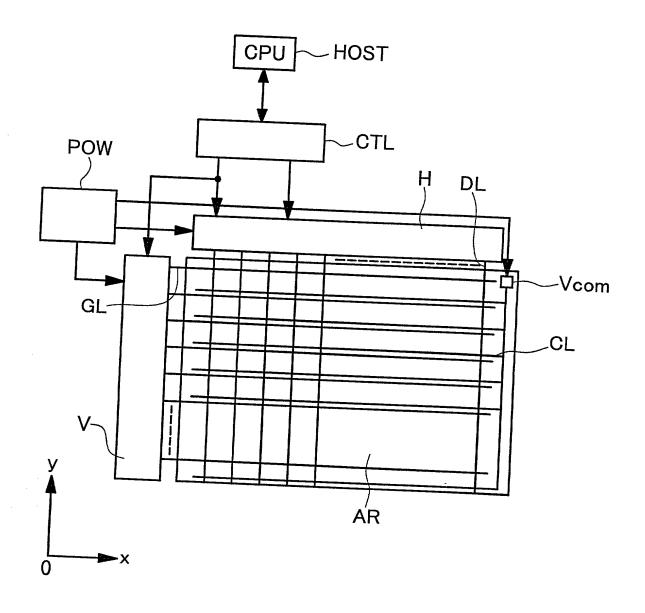
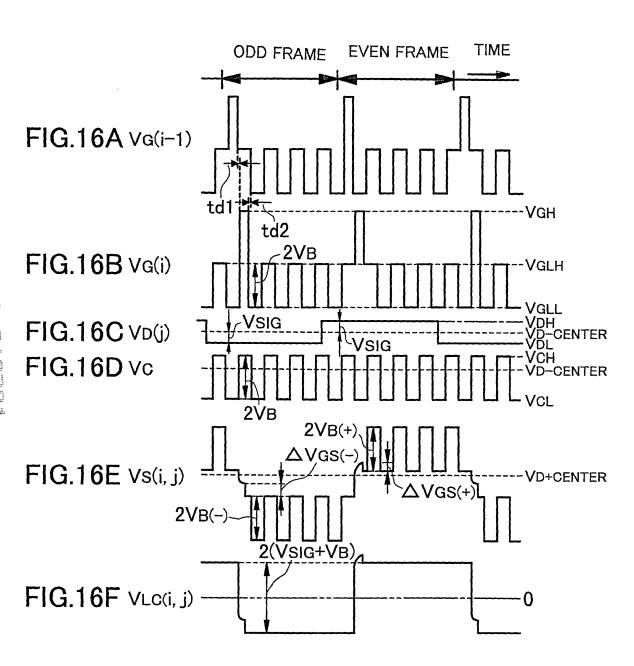


FIG.14

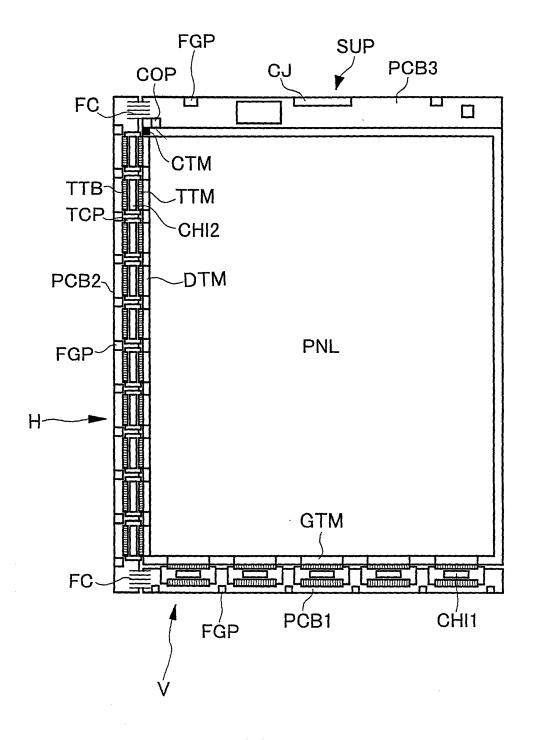


**FIG.15** 





**FIG.17** 



**FIG.18** 

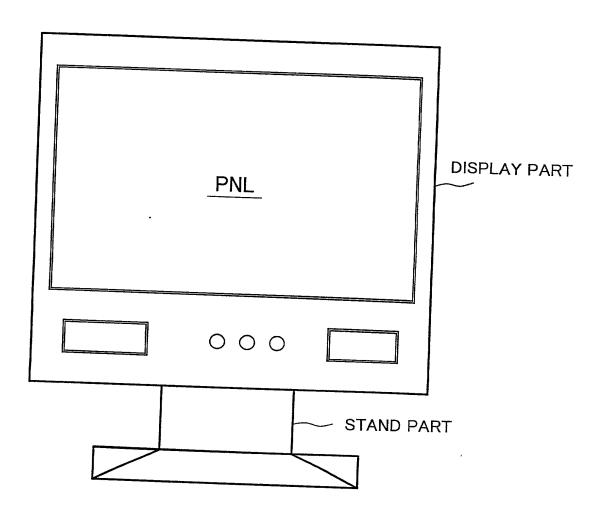
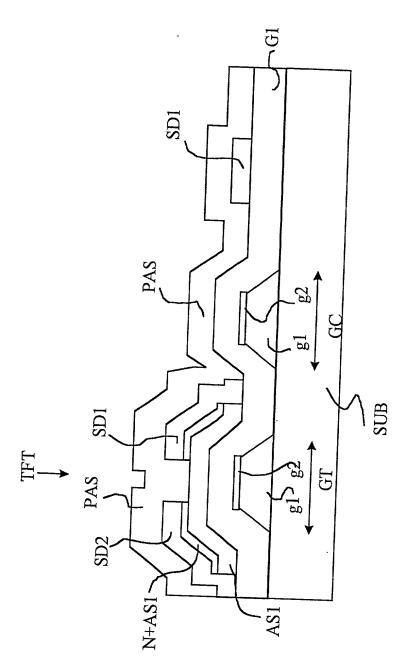


Fig. 19



PX SUB SD1 GTSD2 N+AS1~

Fig. 21

